

NEW

PATENT

AMENDMENT A (PRELIMINARY)

SPECIFICATION AMENDMENTS

At page 1, lines 1-2, please amend the Title as follows:

MICROPROCESSOR WITH HARDWARE CONTROLLED POWER
MANAGEMENT SIGNAL-INITIATED METHOD FOR SUSPENDING
OPERATION OF A PIPELINED DATA PROCESSOR

At page 1, between lines 2 and 3, please insert the following:

RELATED APPLICATIONS

now U.S. Pat. No. 6,721,294

This is a division of U.S. patent application no. 10/216,615, filed August 9,
2002.

At page 1, lines 4-6, please amend the text of the section entitled "Technical Field of the Invention" as follows:

This invention relates in general to integrated circuits, and more particularly to a microprocessor having hardware controlled pipelined data processor with power management control.

At page 4, lines 3-21, please amend the text of the section entitled "Summary of the Invention" as follows:

~~—In accordance with the present invention, a method and apparatus is provided which provides significant advantages in reducing the power consumption of a microprocessor.~~

~~—In the present invention, a processing unit includes a plurality of subcircuits and circuitry for generating a clock signal thereto. Circuitry is provided for detecting the assertion of a control signal; responsive to the control signal, disabling circuitry disables the clock signal to one or more of the subcircuits.~~